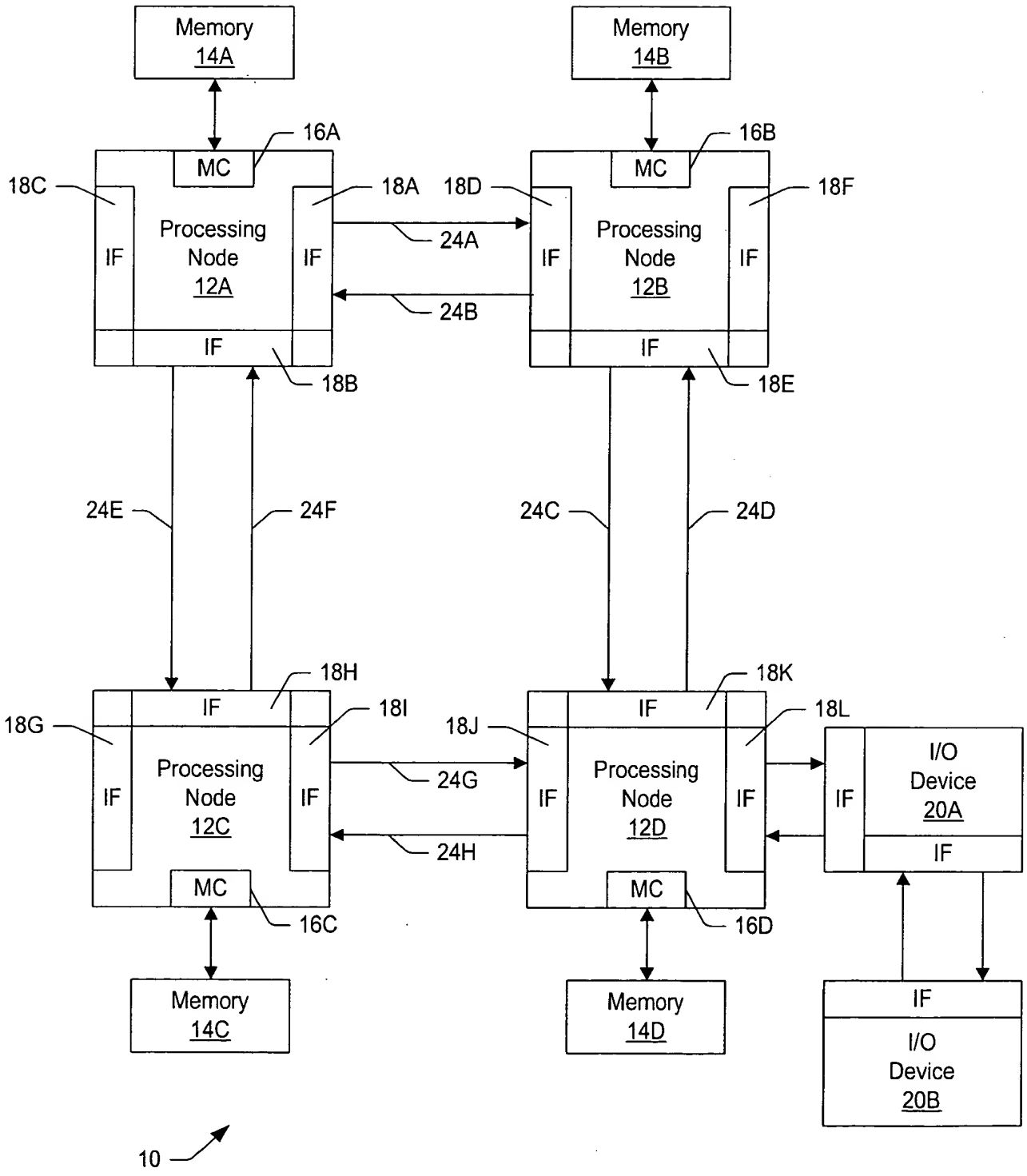
Memory
14A
Memory
14B
16A
16B
MC
MC
18C
18A
18D
18F
IF
Processing
Node
12A
IF
IF
Processing
Node
12B
IF
24A
24B
IF
IF
18B
18E
24E
24F
24C
24D
18H
18K
IF
IF
18G
18I
18J
18L
IF
Processing
Node
12C
IF
IF
Processing
Node
12D
IF
I/O
Device
20A
24G
24H
IF
MC
MC
16C
16D
Memory
14C
Memory
14D
IF
I/O
Device
20B
10

Fig. 1

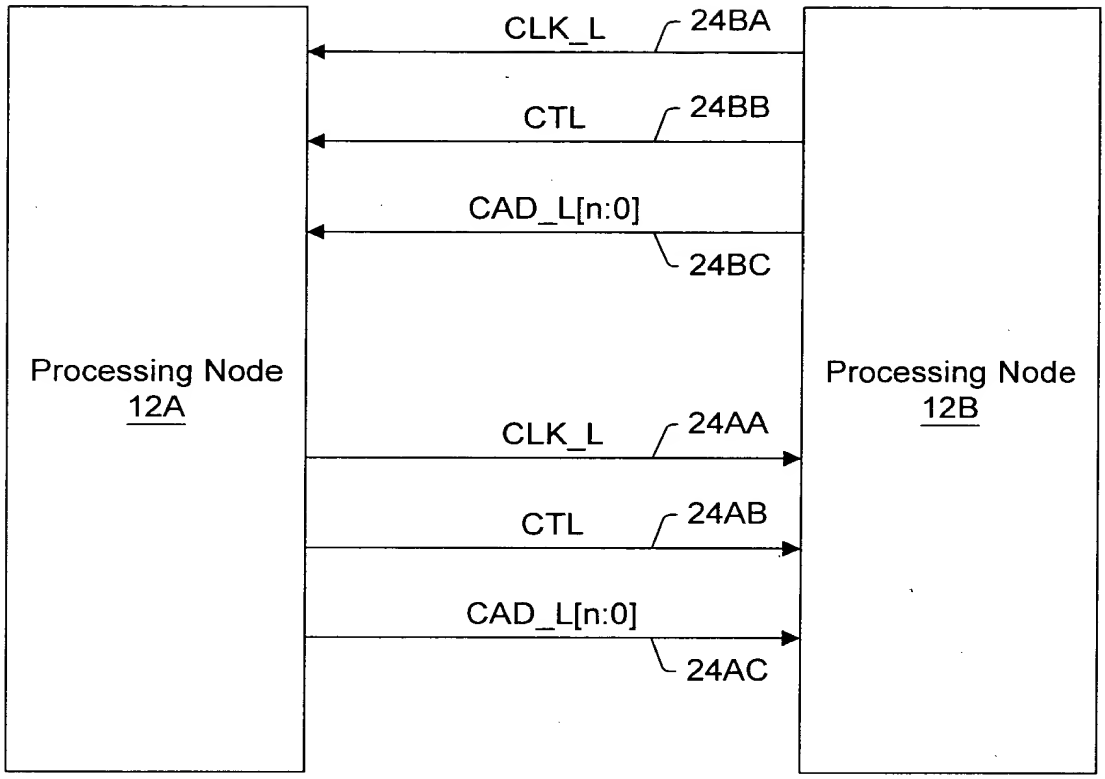

Fig. 2

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Bit Time	7	6	5	4	3	2	1	0
1			CMD[5:0]					
2								
3								
4								

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Fig. 3

Bit Time	7	6	5	4	3	2	1	0
1	SrcUnit [1:0]		CMD[5:0]					
2	DestNode[2:0]		DestUnit [1:0]		SrcNode[2:0]			
3			SrcTag[4:0]					
4								
5	Addr[15:8]							
6	Addr[23:16]							
7	Addr[31:24]							
8	Addr[39:32]							

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Fig. 4

09/05/02, 07:13:04

Bit Time	7	6	5	4	3	2	1	0
1	SrcUnit [1:0]			CMD[5:0]				
2	DestNode[2:0]			DestUnit [1:0]		SrcNode[2:0]		
3				SrcTag[4:0]				
4								

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Fig. 5

Bit Time	7	6	5	4	3	2	1	0
1	Data[7:0]							
2	Data[15:8]							
3	Data[23:16]							
4	Data[31:24]							
5	Data[39:32]							
6	Data[47:40]							
7	Data[55:48]							
8	Data[63:56]							

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Fig. 6

09505802 070104

CMD Code	Command	Packet Type
000000	Nop	Info
000001	VicBlk	Command/Data
000010	Reserved	
000011	ValidateBlk	Command
000100	RdBlk	Command
000101	RdBlkS	Command
000110	RdBlkMod	Command
000111	ChangetoDirty	Command
x01xxx	WrSized	Command/Data
01xxxx	ReadSized	Command
100xxx	Reserved	
110000	RdResponse	Response/Data
110001	ProbeResp	Response
110010	TgtStart	Response
110011	TgtDone	Response
110100	SrcDone	Response
110101	MemCancel	Response
11011x	Reserved	
11100x	Probe	Command
11101x	Broadcast	Command
11110x	Reserved	
111110	Reserved	
111111	Sync	Info

38

Fig. 7

Docket No. 50R5090

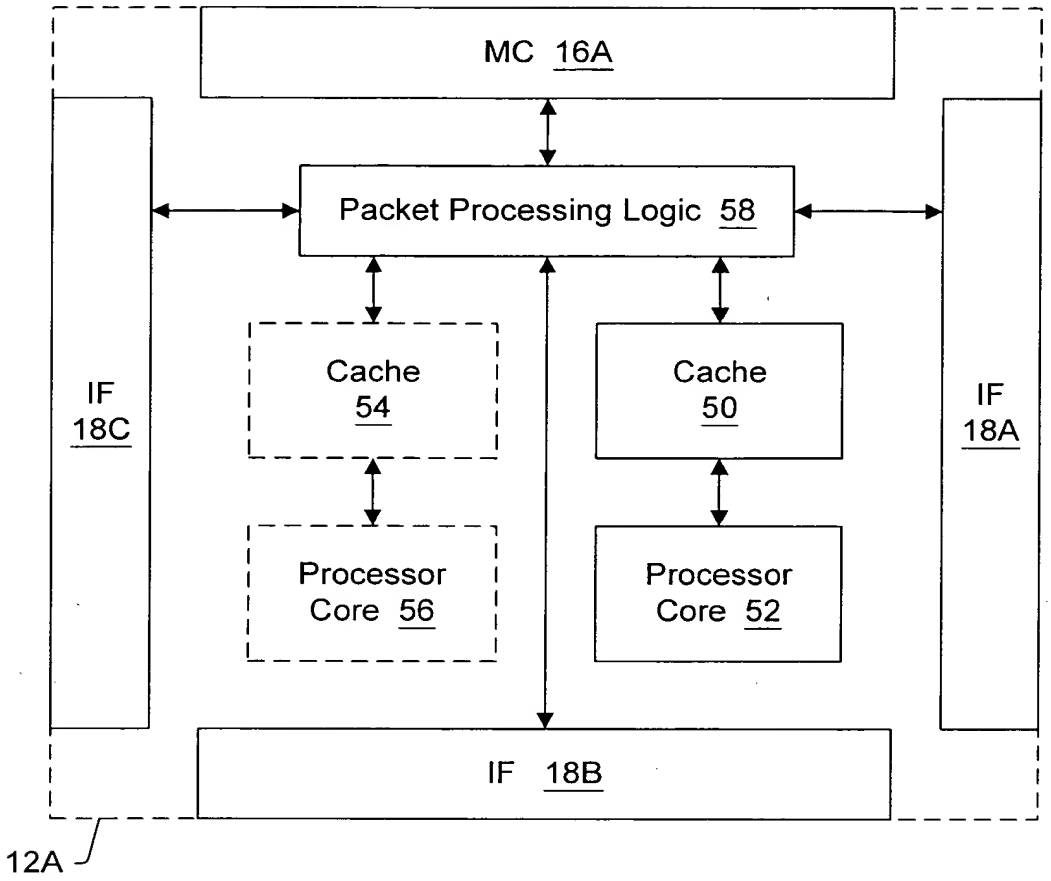

Fig. 8

EP 2 575 328 A1

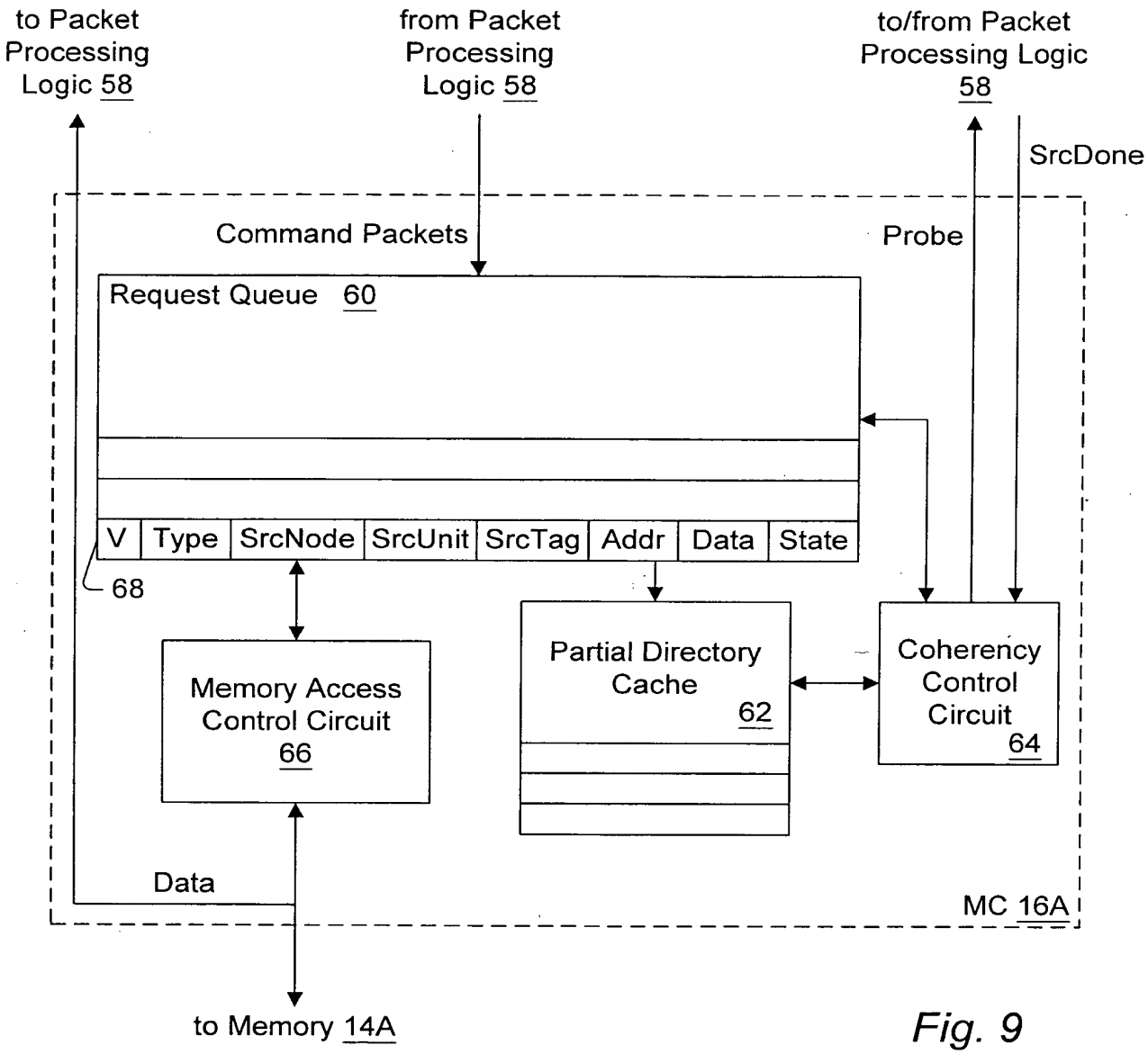
to Packet Processing Logic 58
from Packet Processing Logic 58
to/from Packet Processing Logic 58
SrcDone
Command Packets
Probe
Request Queue 60
V
Type
SrcNode
SrcUnit
SrcTag
Addr
Data
State
68
Memory Access Control Circuit 66
Partial Directory Cache 62
Coherency Control Circuit 64
Data
MC 16A
to Memory 14A

Fig. 9

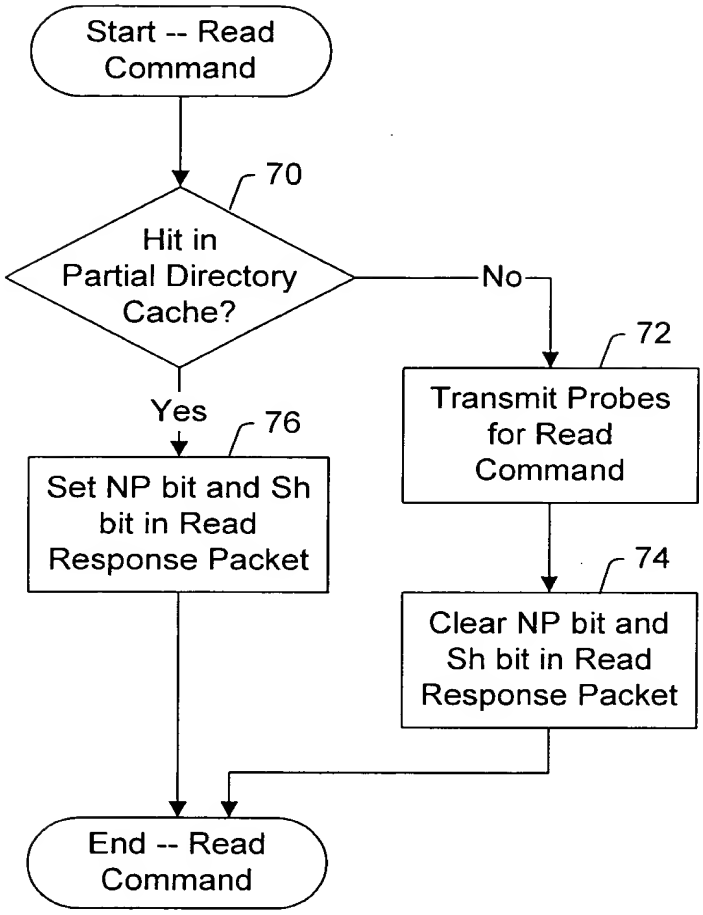

Fig. 10

0960502-070804

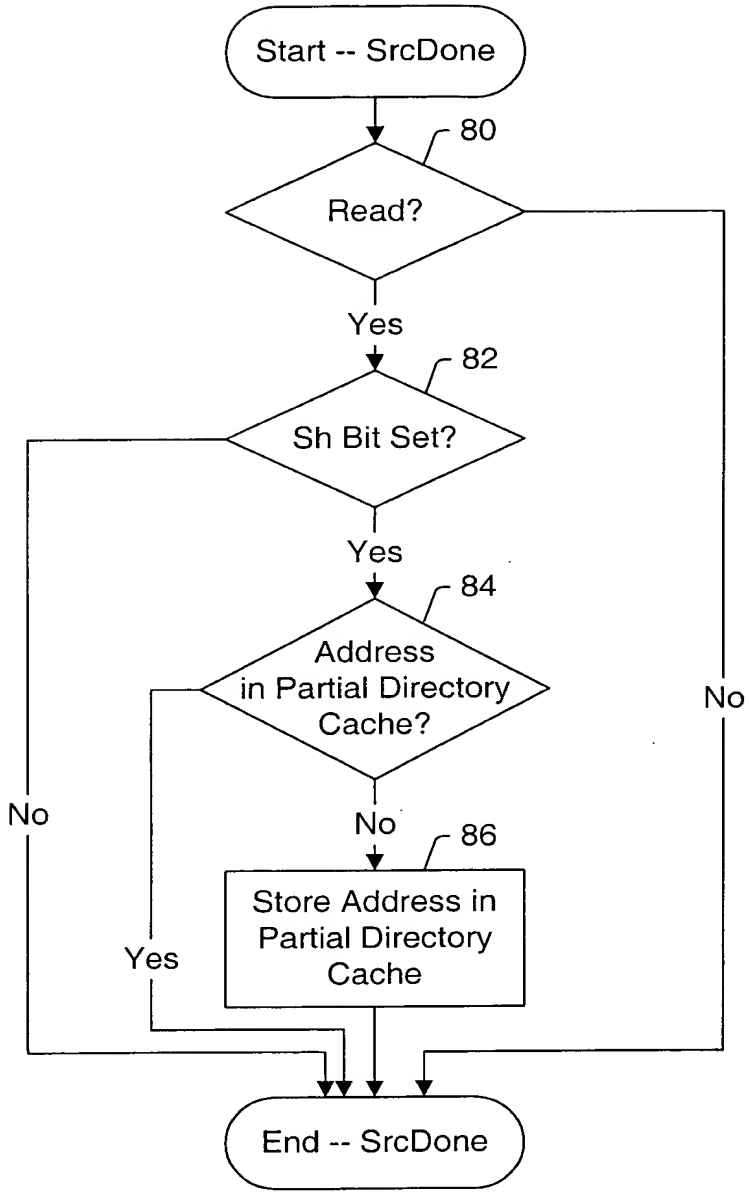
Start -- SrcDone
80
Read?
Yes
82
Sh Bit Set?
Yes
84
Address in Partial Directory Cache?
No
No
86
Store Address in Partial Directory Cache
Yes
No
End -- SrcDone

Fig. 11

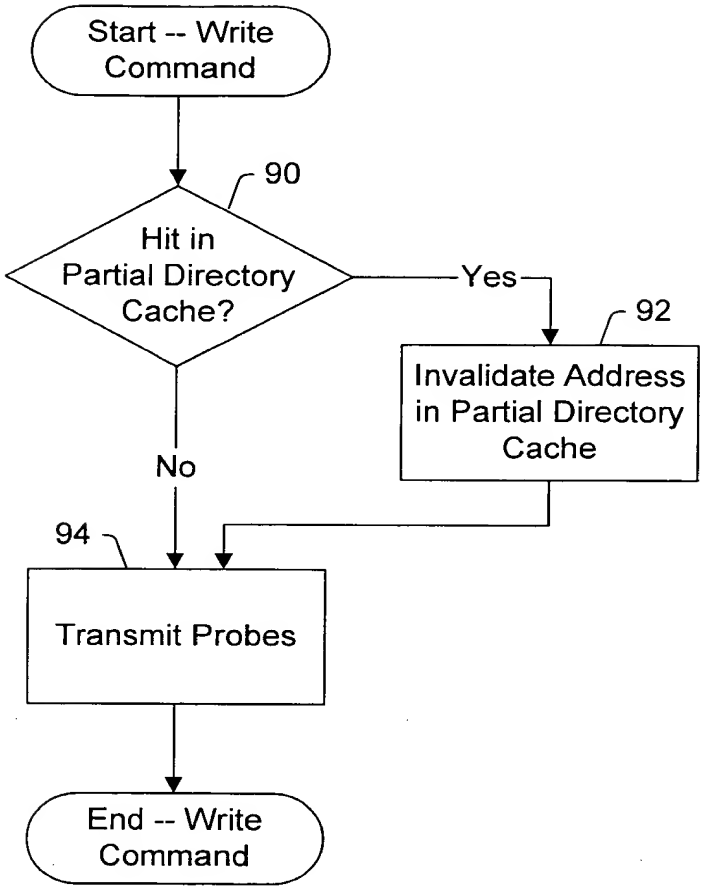

Fig. 12

09/950,022 07/12/01

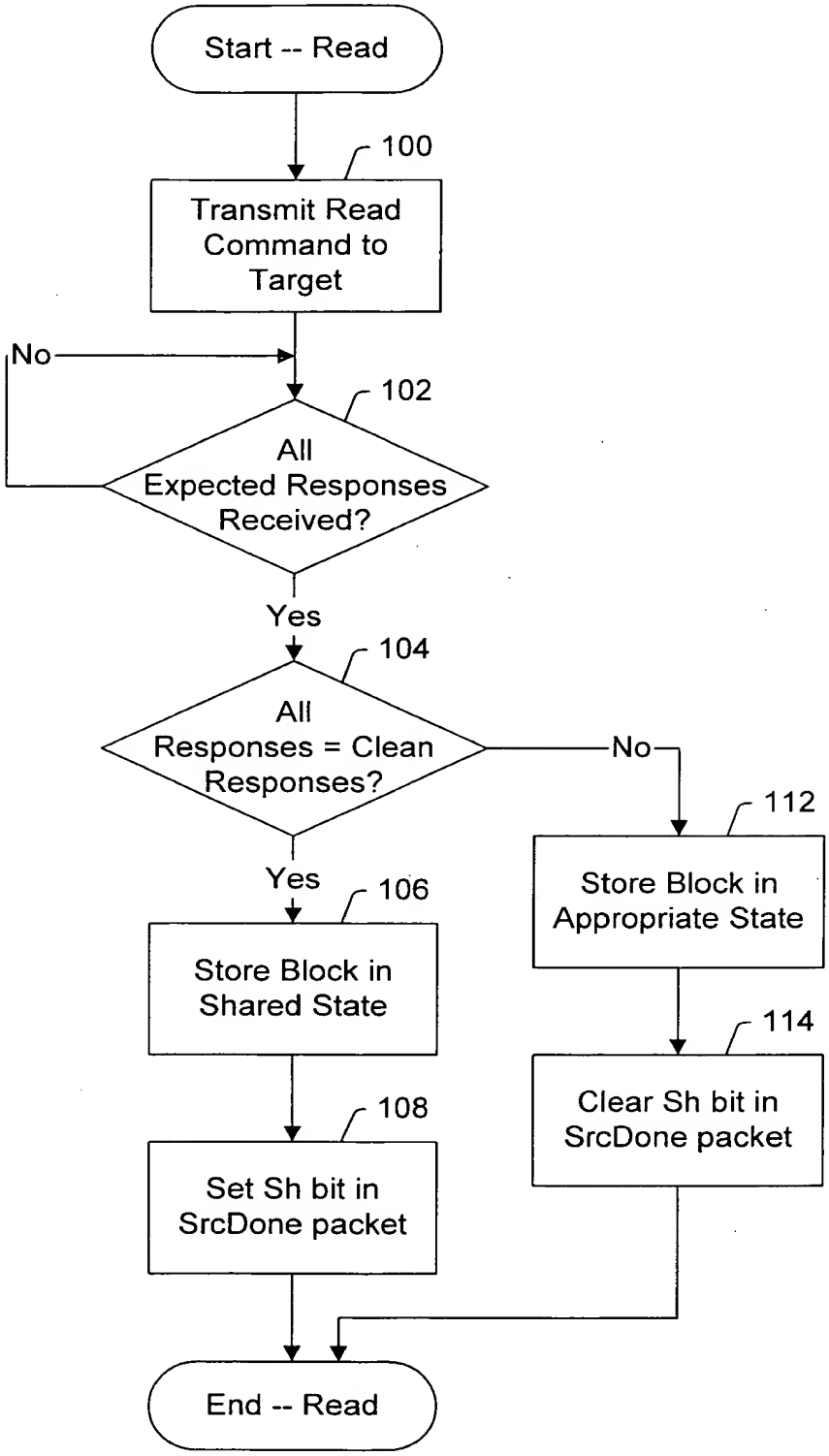

Fig. 13

EP 0 976 059 B1

V	Page Address	B0V	B1V	B2V	B3V	. . .	B63 V

110

Fig. 14

Bit Time	7	6	5	4	3	2	1	0
1	SrcUnit [1:0]		CMD[5:0]					
2	DestNode[2:0]			DestUnit [1:0]		SrcNode[2:0]		
3				SrcTag[4:0]				
4	Sh							

122

120

Fig. 15

Bit Time	7	6	5	4	3	2	1	0
1	SrcUnit [1:0]		CMD[5:0]					
2	DestNode[2:0]			DestUnit [1:0]		SrcNode[2:0]		
3	Count[1:0]		Err	SrcTag[4:0]				
4	Sh	P	NP			Cncl	Count[3:2]	

134 132

130

Fig. 16